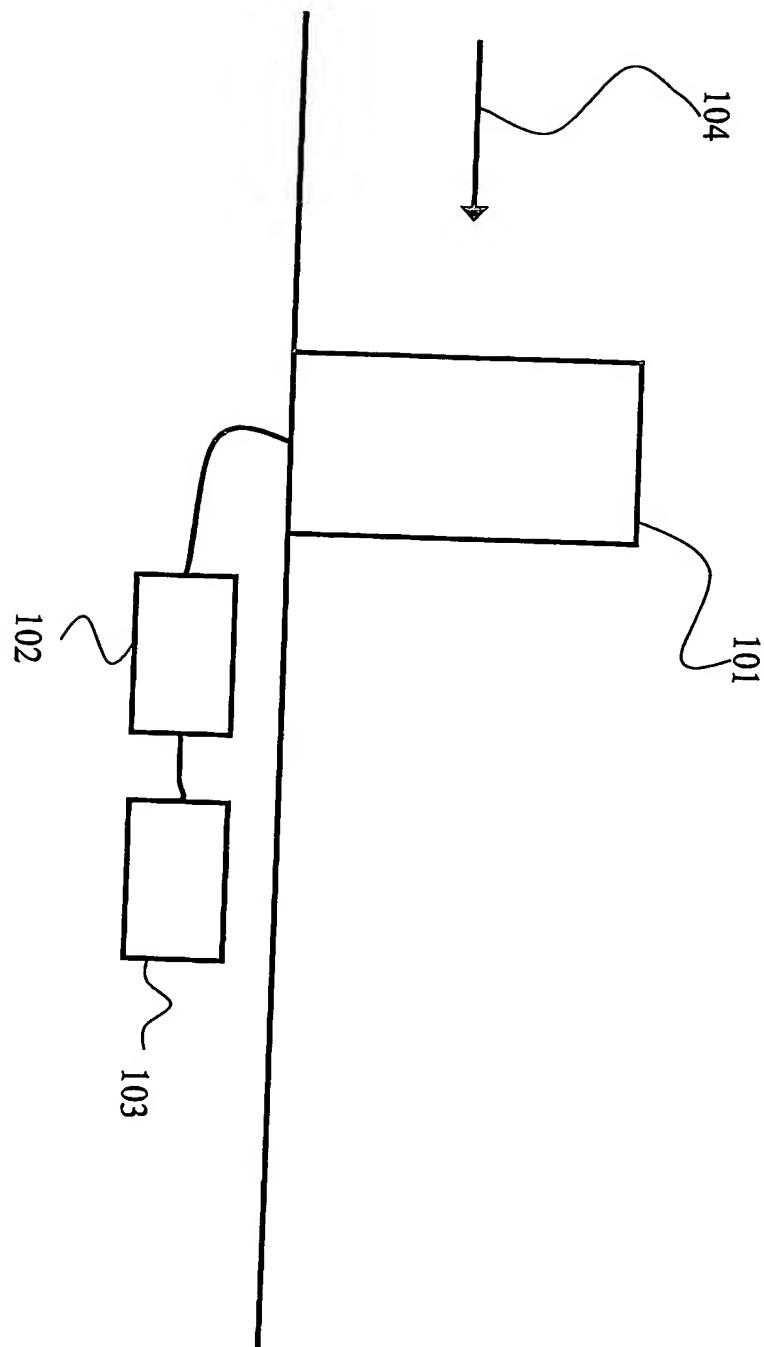


Fig. 1



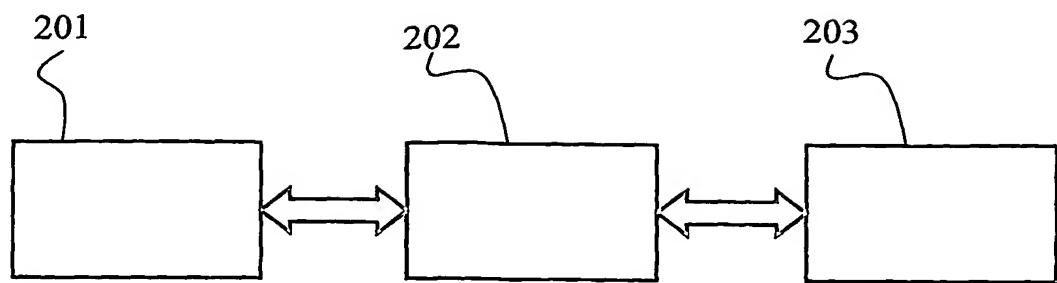


Fig. 2

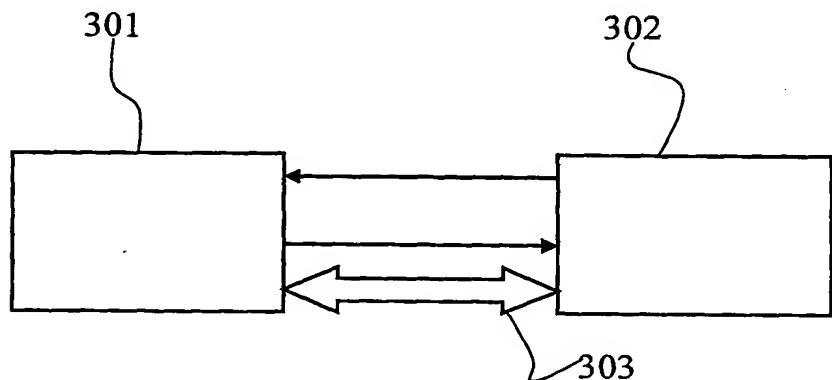


Fig. 3

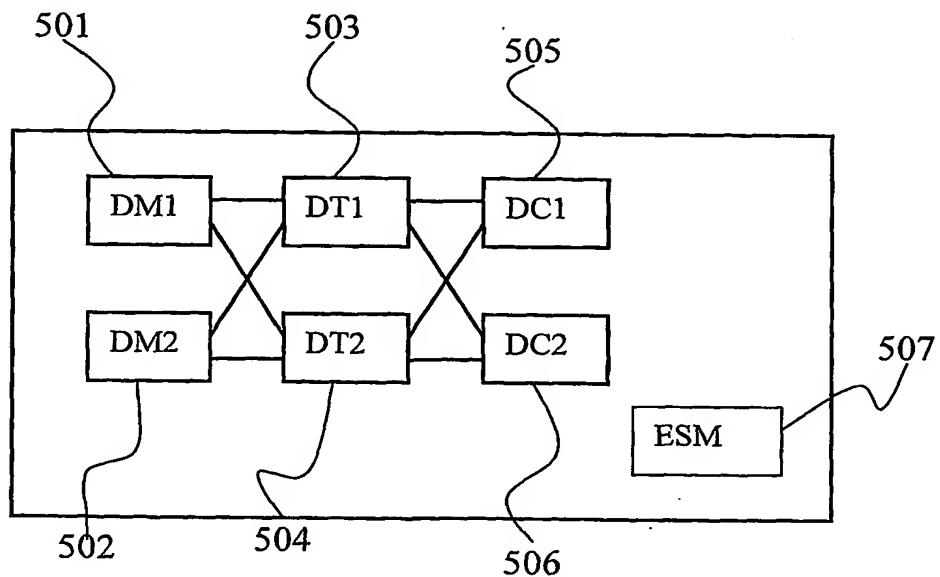


Fig. 5

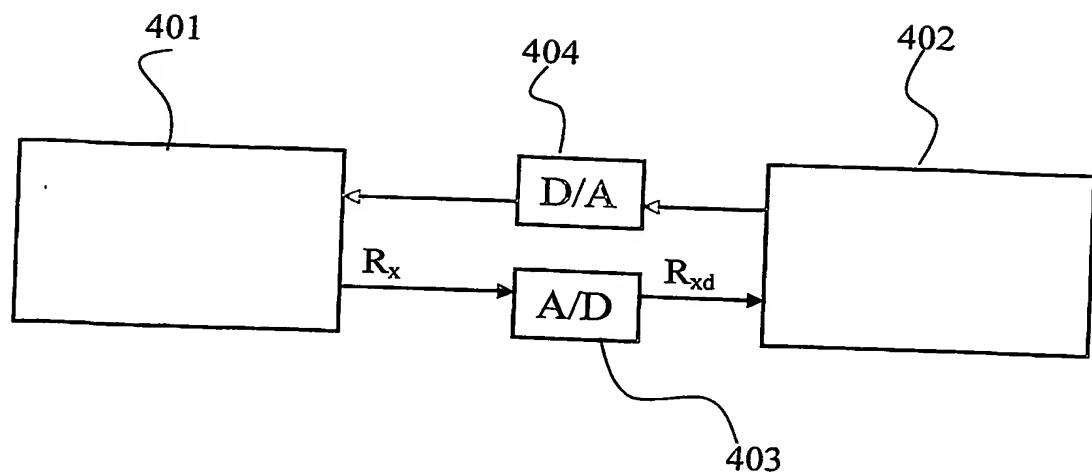


Fig. 4

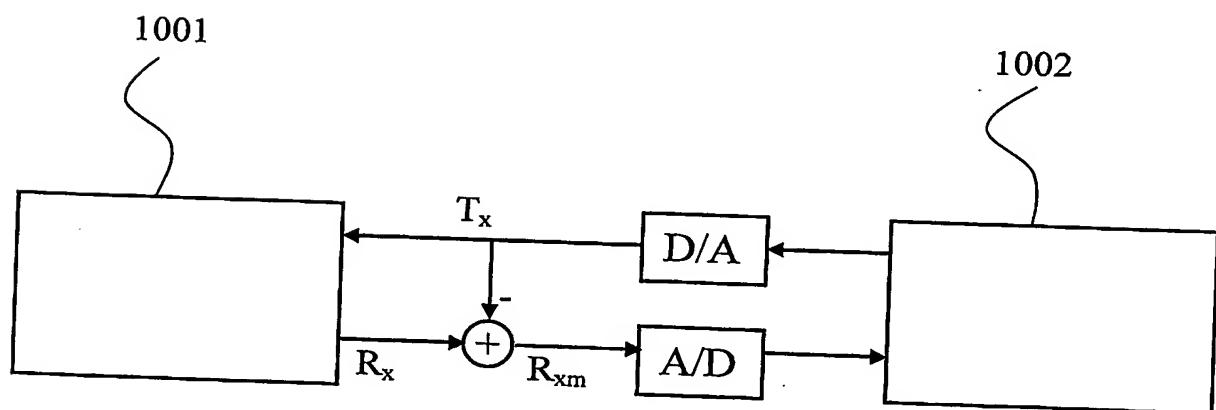


Fig. 10

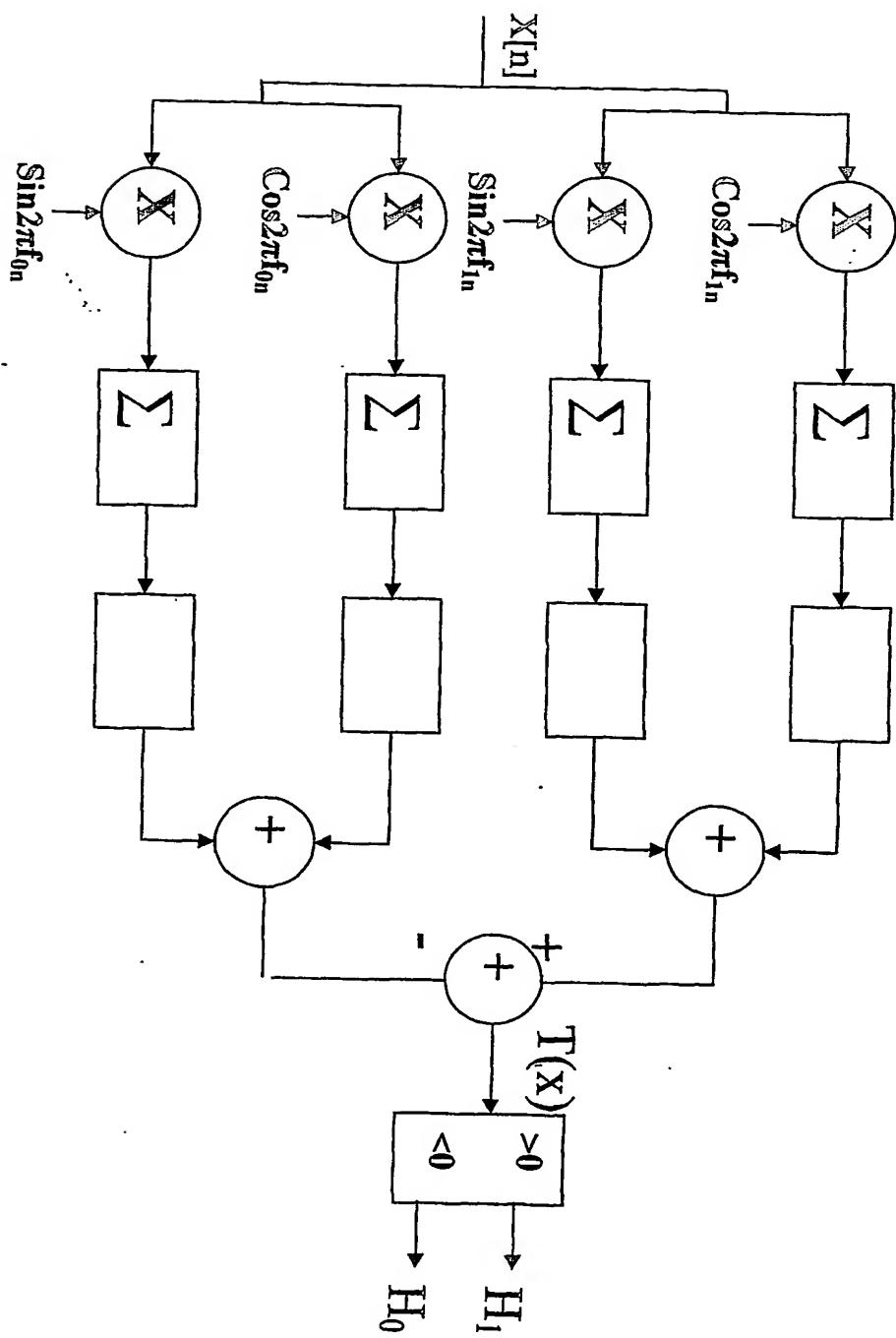


Fig. 6

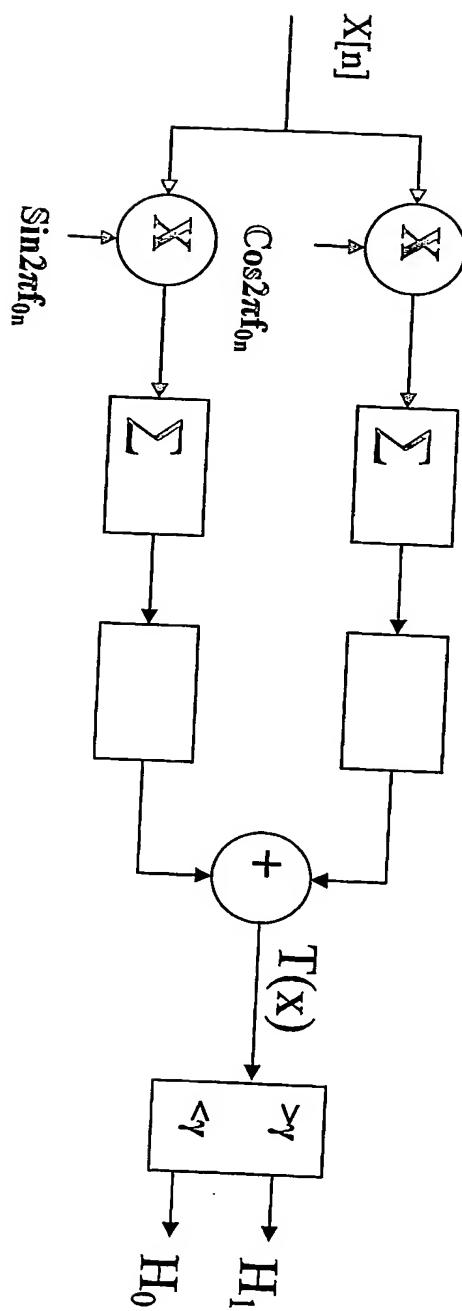


Fig 7

Start Sequence (11 bits)	Block 1 (9 bits)	Block 2 (9 bits)	Block 13 (9 bits)
0 0 0 0 0 0 0 0 x x x x x 1 x x x x x x x 1	x x x x x x x 1		

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Start Sequence (8bits)	Identity Code (64 bits)	CRC (16 bits)	Not Used (24 bits)
0 1 1 1 1 0 x x x	... x x x x x x x	... x x x x x x x	... x x x x x x x

Fig. 9